

Docket No.: 4752-006

JC20 Rec'd PCT/PTO 30 JUN 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Nobuyuki YAMASAKI

U.S. Patent Application No. *not yet assigned*Filed: *herewith*

For: CONTEXT SWITCHING METHOD, CONTEXT SWITCHING UNIT, CONTEXT SWITCHING PROGRAM, STORAGE MEDIUM, AND CENTRAL PROCESSING UNIT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed with the application and no certification or fee is required.

Please note this is a PCT application in the entry of the National Phase in the U.S. and copies of the references cited were transmitted by WIPO and are believed to be in the file of the above-identified application and readily available to the Examiner. Therefore it is believed that Applicants have met all requirements regarding duty of disclosure under 37 CFR 1.56. A copy of the foreign search report is attached for the Examiner's information. Acknowledgement and consideration of these documents are respectfully requested.

Respectfully submitted,

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)		ATTY. DOCKET NO. 4752-006	U.S. APPLICATION NO. 10/1541187 Not yet assigned			
		APPLICANT Nobuyuki YAMASAKI				
		FILING DATE herewith	GROUP			
U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME			
FOREIGN PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation Yes No
	2000-242505A	09-08-00	JP			
	3-9431 A	01-17-91	JP			
	10-11301 A	03-14-00	JP			
	2000-76079 A	03-14-00	JP			
	2002-513182A	05-08-02	JP			
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
	Guangzuo, C. et. al, Parallel Replacement Mechanism for MultiThread, Advances in Parallel and Distributed Computing 1997 Proceedings, 1997, pages 338 to 344; particularly, pages 340,					
	Wills, D.S. et al., Pica: An Ultra-Light Processor for High-Troughput Application, Computer Design: VLSI in Computers and Processors, 1993. ICCD '93. Proceedings, 1993, pages 410 to 414; particularly, pages 411 to 412.					
	Kwak, H. et al., Effects of multithreading on cache performance, computers, IEEE Transactions on, 1999, pages 176 to 184.					
EXAMINER		DATE CONSIDERED				

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.